XILINX JTAG SMT DIGILENT USB

Xilinx Download Line Xilinx Platform Cable USB Downloader CPLD/FPGA Emulator

Features:

* High speed and stability, complete functions .

JTAG download rate up to 30M , improve work efficiency and save waiting time;

Support Vivado, ISE, SDK, IMPACT, Chipscope, EDK , etc.

(ISE12.1+, Vivado12.1+ Cable plug-in support) It is recommended that ISE use version 14.1 or higher (with its own driver).

* Support updated and upgraded devices

Support all FPGA chips

>Zynq-7000 series

>Kintex UltraScale+

>Virtex UltraScale+

>Artix-7

>Kintex-7

>Virtex-7

>Virtex-6

>Spartan-6

+ CoolRunner/CoolRunner-II CPLD series

+ Xilinx Platform Flash ISP configuration PROM series

+ Third party SPI PROMs

+ Third party SPI BPI PROMs

 9500/9500XL CPLD and 1700 and 18V00 ISP PROM devices are not supported .

* The following operating systems are supported :

—Microsoft Windows 7\Windows 8\Windows 10
—Microsoft Windows XP Professional —Windows 2000 —Red
Hat Enterprise Linux
—SUSE Linux Enterprise

* Automatically judge and adapt to the target I/O voltage

* Compatible with the indicated device voltage range:
  5V (TTL), 3.3V (LVCMOS), 2.5V, 1.8V and 1.5V; **the reference voltage is consistent with the official one, and it has the function of preventing wrong insertion or high voltage damage to the downloader and board.**

Shipping list:

High configuration:

1. One Xilinx SMT2 high - speed USB downloader

2.One USB printing cable

3. One multi-function adapter board

4. One 14-pin 20cm long cable spacing 2.0mm

5. One 14-pin 20cm long cable spacing 2.54mm

6. 10-needle 20cm long line spacing 2.54mm One piece of

7, 7 core hole 20cm long Dupont line spacing 2.54mm One piece of

8, 7 core hole 20cm Dupont line spacing 2.54mm (with signal logo) One piece

9. Data network disk:

Standard configuration:

1. One Xilinx SMT high-speed USB downloader

2. One USB2.0 data cable

3. One 2.54mm-14P   cable

4. Data network disk